

A Technical Seminar Report on Recent Trends in VLSI & NETWORKING

The department of ECE has organized "A Technical Seminar on Recent Trends in VLSI & NETWORKING" on 27/08/2018. Students of III & IV Year B.Tech ECE students are participated in this seminar.

Resource Persons: Mr.M.Shanmuga Kumar & Mr.M.Dayala Kumar, Ph.D Scholars from IITDM Kancheepuram.

Contents: (Covered in Mr.M.Shanmuga Kumar Presentation)

- Advanced Topics in Semiconductor Technology - Three-Dimensional Transistor Technologies.
- Mark Bohr encoge para explicar 22nm.
- Era of 3D Transistors.
- Key objectives of Transistor design.
- Tri-Gate Transistors.
- Fabrication of Integrated circuits.
- The Making of a Chip with 22nm/3D Transistors.
- The Most Sophisticated Manufacturing Process in the World | Inside the Fab | Intel.
- How a CPU is Made / Microchips (Animation).
- Moore's Law



